

PRINTER RUSH
(PTO ASSISTANCE)

Application : 09586557 Examiner : LEVITAN GAU : 2662

From: NPB Location: IDC FMF FDC Date: 02/06/06

Tracking #: epm 09586557 Week Date: 12/26/05

DOC CODE	DOC DATE	MISCELLANEOUS
<input type="checkbox"/> 1449		<input type="checkbox"/> Continuing Data
<input type="checkbox"/> IDS		<input type="checkbox"/> Foreign Priority
<input type="checkbox"/> CLM		<input type="checkbox"/> Document Legibility
<input type="checkbox"/> IIFW		<input type="checkbox"/> Fees
<input type="checkbox"/> SRFW		<input type="checkbox"/> Other
<input checked="" type="checkbox"/> DRW	<u>05/31/00</u>	
<input type="checkbox"/> OATH		
<input type="checkbox"/> 312		
<input type="checkbox"/> SPEC		

ATTN: CHIEF DRAFTSPERSON OF THE OFFICE OF PATENT PUBLICATION

[RUSH] MESSAGE:

Please provide replacement drawing sheet for Figure 2.
(Letter "s" was added in the drawing sheet dated 05/31/00 for
Figure 2 in the word "system").

Thomley

[XRUSH] RESPONSE: 2/15/06

DRAWING CORRECTED

INITIALS: LAM

NOTE: This form will be included as part of the official USPTO record, with the Response document coded as XRUSH.

REV 10/04

Diagram illustrating a network architecture (10) connected to a Local Area Network (LAN) (24). The architecture includes:

- Client Terminal (12)
- Client Terminal (14)
- Multi-Point Control Unit (16)
- Gateway (18)
- Gatekeeper (20)
- Server (22)

The Client Terminal (12), Client Terminal (14), Multi-Point Control Unit (16), and Gateway (18) are connected to the LAN (24). The Gatekeeper (20) and Server (22) are also connected to the LAN (24).

The diagram illustrates a hierarchical system architecture across three levels:

- Network Level 30:** Contains a **Server** and a **Standby Server** (dashed circle). They are connected by a bidirectional dashed arrow.
- System Level 32:** Contains five components: **Gate-keeper**, **Client 1**, **Client 2**, **MCU**, and **Gate-way**. These are all connected to the **Server** at Network Level 30 via a central horizontal line with downward arrows.
- Component Level 34:** Contains three components: **I/O Card**, **CPU**, and **Standby CPU** (dashed circle). The **I/O Card** and **CPU** are connected to the **Gate-keeper** at System Level 32 via a horizontal line with downward arrows. The **Standby CPU** is connected to the **Gate-keeper** via a dashed arrow.

FIG. 2